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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: James D. Gallia, et al.
Serial No.: 10/035,606
Filed: December 28, 2001
Title: METHOD AND SYSTEM FOR REDUCING CHARGE DAMAGE
IN SILICON-ON-INSULATOR TECHNOLOGY
Grp./A.U.: 2811
Examiner: Shouxiang Hu Confirmation No.: 3751

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being electronically filed
with United States Patent and trademark Office on:

January 19, 2007 (Date)

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/Elizabeth Schumacher/

(Signature of the person signing the certificate)

Sir:

AMENDMENT UNDER 37 C.F.R. § 1.116

The Applicants have carefully considered this application in connection with the Examiner's Final Rejection mailed September 19, 2006, and respectfully request reconsideration of this application in view of the following amendment and remarks.